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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITING PROCEDURE
EXAMINING GROUP 2800

OCT 18 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tseng
Serial No.: 10/828,884
Filed: April 21, 2004
For: Compensated Linearity Voltage-Control-Capacitor Device by Standard CMOS Process

Docket No.: TSM03-0763
Art Unit: 2823
Examiner: Khiem D. Nguyen

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. § 1.116

Dear Sir:

The following remarks are presented in response to the Examiner's Office Action mailed September 6, 2006.